

14 Pin Single-in-Line Package Passive Delay Lines

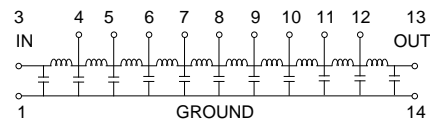
Zo OHMS ±10%	DELAY nS ±5% or ±2 nS†	TAP DELAYS ±5% or ±2 nS†	RISE TIME nS Max.	DCR OHMS Max.	PCA PART NUMBER	Zo OHMS ±10%	DELAY nS ±5% or ±2 nS†	TAP DELAYS ±5% or ±2 nS†	RISE TIME nS Max.	DCR OHMS Max.	PCA PART NUMBER
50	10	1 ±0.5	2	1	EP6700-1	100	20	2 ±0.5	4	4	EP6700-11
50	20	2 ±0.5	4	1	EP6700-2	100	40	4 ±0.5	8	4	EP6700-12
50	30	3 ±0.5	6	1	EP6700-3	100	60	6 ±1.0	12	4	EP6700-13
50	40	4 ±0.5	8	1	EP6700-4	100	80	8 ±1.0	16	4	EP6700-14
50	50	5 ±1.0	10	1	EP6700-5	100	100	10	20	4	EP6700-15
50	60	6 ±1.0	12	2	EP6700-6	100	120	12	24	6	EP6700-16
50	70	7 ±1.0	14	2	EP6700-7	100	140	14	28	6	EP6700-17
50	80	8 ±1.0	16	2	EP6700-8	100	160	16	32	6	EP6700-18
50	90	9 ±1.0	18	2	EP6700-9	100	180	18	36	6	EP6700-19
50	100	10	20	2	EP6700-10	100	200	20	40	6	EP6700-20

† Whichever is greater.

Optional: Output internally terminated, add "T" after PCA P/N; ex. EP6700-IT

DC Electrical Characteristics	Min	Max	Unit
Distortion		±10	%
Temperature Coefficient of Delay		100	PPM/°C
Insulation Resistance @ 100 Vdc	1K		Meg Ohms
Dielectric Strength		100	Vdc

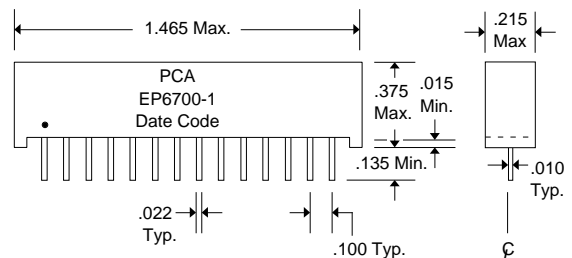
Schematic



Recommended Operating Conditions	Min	Max	Unit
PW*	Pulse Width % of Total Delay	200	%
D*	Duty Cycle	40	%
T _A	Operating Free Air Temperature	0	70 °C

*These two values are inter-dependent.

Package Dimensions



Input Pulse Test Conditions @ 25°C		
V _{IN}	Pulse Input Voltage	3 Volts
PW	Pulse Width % of Total Delay	300 %
T _{RI}	Input Rise Time (10 - 90%)	2.0 nS
PRR	Pulse Repetition Rate @ T _d ≤ 150 nS	1.0 MHz
	Pulse Repetition Rate @ T _d > 150 nS	300 KHz